



PIC16C54 → PIC16C54C Migration

DEVICE MIGRATIONS

This document is intended to describe the functional differences and the electrical specification differences that are present when migrating from one device to the next.

Note: This device has been designed to perform to the parameters of its data sheet. It has been tested to an electrical specification designed to determine its conformance with these parameters. Due to process differences in the manufacture of this device, this device may have different performance characteristics than its earlier version. These differences may cause this device to perform differently in your application than the earlier version of this device.

Table 1 shows the considerations that must be taken into account when migrating from the PIC16C54 to the PIC16C54C.

TABLE 1: PIC16C54 → PIC16C54C DIFFERENCES

Functional Differences				
No.	Difference	H/W	S/W	Prog.
1	Master Clear Filter added, PIC16C54C. See Electrical Specification #30	✓	—	—
2	Code protection change, PIC16C54C now reads as 0 when code protect enabled	—	—	✓
3	Programming algorithm change, PIC16C54C uses a new programming algorithm	—	—	✓

TABLE 2: ELECTRICAL SPECIFICATION DIFFERENCES

Parm. No.	Sym.	Characteristic	PIC16C54 Data Sheet			PIC16C54C Data Sheet			Units	Conditions
			Min	Typ	Max	Min	Typ	Max		
	VDD	Supply Voltage								
		XT mode, commercial, industrial	3.0	—	6.25	3.0	—	5.5	V	
		RC mode, commercial, industrial	3.0	—	6.25	3.0	—	5.5	V	
		LP mode, commercial, industrial	2.5	—	6.25	3.0	—	5.5	V	
		XT mode, extended	3.25	—	6.0	3.0	—	5.5	V	
		RC mode, extended	3.25	—	6.0	3.0	—	5.5	V	
		LP mode, extended	2.5	—	6.0	3.0	—	5.5	V	
	IDD	Supply Current								
		XT mode, commercial	—	1.8	3.3	—	1.8	2.4	mA	Fosc = 4MHz, VDD = 5.5V
		HS mode, commercial	—	9	20	—	4.5	16	mA	Fosc = 20MHz, VDD = 5.5V
		LP mode, commercial	—	15	32	—	14	32	µA	Fosc = 32kHz, VDD = 3.0V
		LP mode, industrial	—	15	40	—	17	40	µA	Fosc = 32kHz, VDD = 3.0V
	IPD	Power-down Current								
		Commercial	—	0.6	9	—	0.25	4.0	µA	VDD = 3.0V, WDT disabled
		Industrial	—	0.6	12	—	0.25	5.0	µA	VDD = 3.0V, WDT disabled
30	TmCL	MCLR Pulse Width (low)	100*	—	—	1000*	—	—	ns	VDD = 5.0V
34	Tioz	I/O Hi-impedance from MCLR Low	—	—	100*	100*	300*	1000*	ns	
	Fosc	External CLKIN Frequency⁽¹⁾	DC	—	4	DC	—	4	MHz	XT osc mode
			DC	—	10	N/A	—	N/A	MHz	10 MHz mode
			DC	—	16	DC	—	20	MHz	HS osc mode (extended)
			DC	—	40	DC	—	200	kHz	LP osc mode
		Oscillator Frequency⁽¹⁾	DC	—	4	DC	—	4	MHz	RC osc mode
			0.1	—	4	DC	—	4	MHz	XT osc mode
			4	—	10	N/A	—	N/A	MHz	10 MHz mode
			4	—	20	DC	—	20	MHz	HS osc mode (Com/Indust)
			4	—	16	DC	—	20	MHz	HS osc mode (Extended)
			DC	—	40	DC	—	200	kHz	LP osc mode

* These parameters are characterized but not tested.

Note 1: All specified values are based on characterization data for that particular oscillator type under standard operating conditions with the device executing code. Exceeding these specified limits may result in an unstable oscillator operation and/or higher than expected current consumption.

Note: The user should verify that the device oscillator starts and performs as expected. Adjusting the loading capacitor values and /or the oscillator mode may be required.

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- The PICmicro family meets the specifications contained in the Microchip Data Sheet.
- Microchip believes that its family of PICmicro microcontrollers is one of the most secure products of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the PICmicro microcontroller in a manner outside the operating specifications contained in the data sheet. The person doing so may be engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
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- Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our product.

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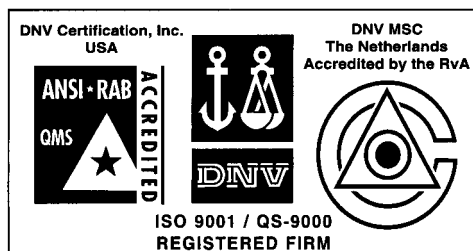
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