

PCN Number:	20150804002	PCN Date:	08/11/2015
Title:	TAS5760L Die Revision Change		
Customer Contact:	PCN Manager	Dept:	Quality Services
Proposed 1st Ship Date:	11/11/2015	Estimated Sample Availability:	Date provided at sample request.
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Assembly Process
<input checked="" type="checkbox"/>	Design	<input type="checkbox"/>	Electrical Specification
<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Packing/Shipping/Labeling
<input type="checkbox"/>	Wafer Bump Site	<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>	Wafer Fab Site	<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Part number change

PCN Details

Description of Change:

This notification is to announce a die revision and datasheet change to the devices listed in the Product Affected Section of this document. The new die provides clocking improvements that allow the MCLK pin to be tied directly to SCLK for some clock ratios. The die also includes changes of the class-D output FET's from a 30V Fab component to a 20V Fab component.

The Die Revision and the datasheet number will be changing:

TAS5760L:

Current		New	
Die Revision	Datasheet Number	Die Revision	Datasheet Number
B	SLOS782A	C	SLOS782B

The product datasheet(s) is updated as seen in the change revision history below:



TAS5760L

SLOS782B – JULY 2013 – REVISED JULY 2015

TAS5760L General-Purpose I2S Input Class-D Amplifier

Changes from Revision A (October 2013) to Revision B	Page
• Added <i>Pin Configuration and Functions</i> section, <i>ESD Ratings</i> table, <i>Feature Description</i> section, <i>Device Functional Modes</i> , <i>Application and Implementation</i> section, <i>Power Supply Recommendations</i> section, <i>Layout</i> section, <i>Device and Documentation Support</i> section, and <i>Mechanical, Packaging, and Orderable Information</i> section	1
• Changed Features list item, Audio Performance From: R _{LOAD} = 8Ω To: R _{SPK} = 8Ω	1
• Changed From: Voltage at speaker amplifier output pins To: Speaker Amplifier Output Voltage in the Abs Max table	6
• Modified Master clock and Serial Audio Port specifications to reflect the clocking improvements of the device.	7
• Changed the Soft Clipper Control (SFT_CLIP Pin) section.....	27

These changes may be reviewed at the datasheet link provided:

<http://www.ti.com/lit/ds/symlink/tas5760l.pdf>

TAS5760LD:

Current		New	
Die Revision	Datasheet Number	Die Revision	Datasheet Number
B	SLOS781	C	SLOS781A

The product datasheet(s) is updated as seen in the change revision history below:



TAS5760LD

SLOS781A – JULY 2013 – REVISED JULY 2015

TAS5760LD General-Purpose I2S Input Class-D Amplifier With DirectPath™ Headphone and Line Driver

Changes from Original (July 2013) to Revision A	Page
• Added Pin Configuration and Functions section, ESD Ratings table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section	1
• Modified Master clock and Serial Audio Port specifications to reflect the clocking improvements of the device.	6

These changes may be reviewed at the datasheet links provided:

<http://www.ti.com/lit/ds/symlink/tas5760ld.pdf>

Reason for Change:

Improved product performance

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Changes to product identification resulting from this PCN:

Die Rev designator will change as shown in the table and sample label below:

Current	New
Die Rev [2P]	Die Rev [2P]
B	C

Sample product shipping label to indicate die rev location (not actual product label)



MADE IN: Malaysia
2DC: 20:

MSL '2 / 260C / 1 YEAR	SEAL DT
MSL 1 / 235C / UNLIM	03/29/04

OPT:
ITEM: 39
LBL: 5A (L)T0:1750





(1P) SN74LS07NSR
(Q) 2000 (D) 0336
(31T) LOT: 3959047MLA
(4W) TKY (1T) 7523483S12
(P)
(2P) REV: (V) 0033317
(20L) CSO: SHE (21L) CCO: USA
(22L) ASO: MLA (23L) ACO: MYS

Product Affected:

TAS5760LDAP	TAS5760LDCA	TAS5760LDDCA	TAS5760LDDCAR
TAS5760LDAPR	TAS5760LDCAR		

Qualification Report

TAS5760LDAP, TAS5760LDCA, TAS5760LDDCA Die Change
Approve Date 30-Jun-2015

Product Attributes

Attributes	Qual Device: TAS5760LDAP	Qual Device: TAS5760LDCA	Qual Device: TAS5760LDDCA
Assembly Site	TAI	TAI	TAI
Package Family	HTQFP	HTSSOP	HTSSOP
Wafer Fab Supplier	RFAB	RFAB	RFAB
Wafer Process	LBC7	LBC7	LBC7

- QBS: Qual By Similarity
- Qual Devices qualified at LEVEL3-260C: TAS5760LDAP, TAS5760LDCA, TAS5760LDDCA
- Device TAS5760LDDCA contains multiple dies.

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	Test Name / Condition	Duration	Qual Device: TAS5760LDAP	Qual Device: TAS5760LDCA	Qual Device: TAS5760LDDCA
ED	Electrical Characterization	Per Datasheet Parameters	Pass	Pass	-
HBM	ESD - HBM	4000 V	1/3/0	1/3/0	1/3/0
CDM	ESD - CDM	1500 V	1/3/0	1/3/0	1/3/0
LU	Latch-up	(per JESD78)	1/6/0	1/6/0	1/6/0
TC	Temperature Cycle, -65/150C	500 Cycles	1/77/0	1/77/0	-
WBP	Bond Pull	Wires	1/76/0	1/76/0	-
WBS	Ball Bond Shear	Wires	1/76/0	1/76/0	-

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours

- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

Green/Pb-free Status:

Qualified Pb-Free (SMT) and Green

For questions regarding this notice, e-mails can be sent to the regional contacts shown below, or you can contact your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
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