

Notification Number:	20200820001	Notification Date:	Aug. 21, 2020
Title:	Datasheet for F28M35H52C, F28M35H22C, F28M35M52C, F28M35M22C, F28M35E20B		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:	Electrical Specification		

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



F28M35H52C, F28M35H22C, F28M35M52C, F28M35M22C, F28M35E20B

SPRS742K – JUNE 2011 – REVISED JUNE 2020

Changes from December 15, 2017 to June 23, 2020 (from J Revision (December 2017) to K Revision) Page

• Global: Removed F28M35M20B.	<u>1</u>
• Global: Replaced "emulator" with "JTAG debug probe".	<u>1</u>
• Section 1.1 (Features): Added "Three external interrupts" feature.	<u>1</u>
• Section 1.2 (Applications): Updated section.	<u>2</u>
• Section 3.1 (Related Products): Updated section.	<u>9</u>
• Table 4-1 (Signal Descriptions): Updated DESCRIPTION of XRS.	<u>12</u>
• Section 5.3 (ESD Ratings – Commercial): Added ANSI/ESDA/JEDEC JS-002 to charged-device model (CDM).....	<u>32</u>
• Section 5.9.1.1 (Power Management and Supervisory Circuit Solutions): Updated section.	<u>42</u>
• Section 5.10.1.5 (ADC Electrical Data and Timing): Removed "Typical ADC Total Error" figure (was Figure 5-12 in SPRS742J).	<u>63</u>
• Table 5-40 (Electrical Characteristics of the Comparator/DAC): Changed "Comparator response time to PWM Trip Zone (Async)" parameter to "Comparator response time to GPIO".	<u>66</u>
• Table 5-77 (SPI Master Mode External Timing (Clock Phase = 0)): Updated MIN value (for both BRR EVEN and BRR ODD) for Parameter 23, $t_{d(SPC)M}$	<u>135</u>
• Table 5-78 (SPI Master Mode External Timing (Clock Phase = 1)): Updated MIN value (for both BRR EVEN and BRR ODD) for Parameter 23, $t_{d(SPC)M}$	<u>137</u>
• Table 6-7 (Control Subsystem Flash, ECC, OTP, Boot ROM): Changed SIZE (BYTES) value of EPI0 from 2G to 2M. Added associated footnote.	<u>155</u>
• Table 6-11 (Master Subsystem Analog and EPI): Added "Control Subsystem has less address reach to EPI memory than the Master Subsystem" footnote.	<u>160</u>
• Section 6.18 (Code Security Module): Updated section.	<u>225</u>
• Section 7.1 (TI Reference Design): Changed section title from "TI Design or Reference Design" to "TI Reference Design". Updated section.	<u>229</u>
• Section 8 (Device and Documentation Support): Changed "Community Resources" section to "Support Resources" section. Updated section.	<u>230</u>
• Section 8.2 (Tools and Software): Updated section.	<u>231</u>
• Section 8.3 (Documentation Support): Updated section.	<u>232</u>
• Section 8.4 (Related Links): Updated section.	<u>233</u>

The datasheet number will be changing.

Device Family	Change From:	Change To:
F28M35H52C, F28M35H22C, F28M35M52C, F28M35M22C, F28M35E20B	SPRS742J	SPRS742K

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/F28M35H52C>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:

None.

Product Affected:

F28M35E20B1RFPS	F28M35E20B1RFPT	F28M35H22C1RFPS	F28M35H22C1RFPT
F28M35H52C1RFPQ	F28M35H52C1RFPS	F28M35H52C1RFPT	F28M35M22C1RFPS
F28M35M22C1RFPT	F28M35M52C1RFPS	F28M35M52C1RFPS	

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