Product Change Notification - SYST-17JODZ026

Date:	18 Apr 2017
Product Category:	Ethernet Controllers
Affected CPNs:	📕 📗
Notification subject:	Data Sheet - LAN9211 High-Performance Small Form Factor Single-Chip Ethernet Controller with HP Auto-MDIX Support
Notification text:	

SYST-17JODZ026

Microchip has released a new DeviceDoc for the LAN9211 High-Performance Small Form Factor Single-Chip Ethernet Controller with of devices. If you are using one of these devices please read the document located at LAN9211 High-Performance Small Form Factor Single-Chip Ethernet Controller with.

Notification Status: Final

Description of Change: 1) Replaces previous SMSC version Rev. 2.9
2) Changed from: "After any PHY reset, the application must wait until the "Link Status" bit in the PHY's "Basic Status Register" (PHY Reg. 1.2) is set before attempting to transmit or receive data."
To: "After any PHY reset, the application should wait until the "Link Status" bit in the PHY's "Basic Status Register" (PHY Reg. 1.2) is set before attempting to transmit data, otherwise data written to the TX FIFO will only be sent when the Link Status returns to "Up"."

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 18 April 2017

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):	LAN9211 High-Performance Small Form Factor Single-Chip Ethernet Controller with

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

LAN9211-ABZJ