

PCN Number:	20180711001	PCN Date:	July 16, 2018
Title:	Datasheet for LP8863-Q1		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



LP8863-Q1

SNVSAB6E]—MARCH 2017—REVISED JULY 2018

Changes from Revision A (June 2017) to Revision B	Page
• Added top navigator link for TI reference design	1
• Added note "When internal charge pump is disabled and CPUMP pin is used as an input, max rating is 5.5 V same as VDD."	7
• Added note "When internal charge pump is disabled and CPUMP pin is used as an input, max rating is 5.5V same as VDD."	7
• Deleted " f_{osc} " row	8
• Deleted "led_driver_headroom = 01b"	9
• Changed to "325" from "200"	9
• Changed to "450" from "400"	9
• Changed to "18.6" from "18.8"	11
• Changed to "21.4" from "21.2"	11
• Added VSDO _{OL} and VSDO _{OH} two rows.....	11
• Changed Data hold time to rising edge of SCLK in SPI Timing Diagram	12
• Changed to "VDD" from "EN"	14
• Changed to "90%" from "88%"	18
• Changed to "1.21 V" from "1.2 V"	19
• Changed to "1.21 V" from "1.2 V"	20
• Changed to "1.76 V" from "1.423 V"	20
• Changed to "1.21 V" from "1.2 V"	21
• Changed to "I _{SEL_MAX} " from "ISEL_MAX"	22
• Changed to "1.21 V and 2560" from "1.2 V and 2580" in Equation 11	24

• Changed to "DITHER_SELECT[2:0]" from "DITHER_SELECT[1:0]"	29
• Changed to "110 ms" from "1600 milliseconds"	34
• Changed to "overcurrent" from "undervoltage"	34
• Changed to "Boost does not start up. Fault is cleared by VDD cycling with correct resistor connection at FSET pin." from "Device starts up using fail-safe values."	36
• Changed to "400-ms" from "300-ms"	39
• Changed to "VDD" from "EN"	39
• Added " For proper operation of discharge function, pull the EN pin before VDD pin. VIN and VDDIO can be set low any time after EN low."	41
• Changed to "8B0h" from "B0h"	49
• Changed to "R/W-2h" from "R/W-0h"	49
• Changed to "2h" from "0h"	49
• Changed to "100h" from "0h"	57
• Changed to "100h" from "0h"	57
• Changed to "100h" from "0h"	57
• Changed to "100h" from "0h"	58
• Changed to "000h - 0FFh = 0 °C to 255 °C, 100h - 1FFh = -256 °C to -1 °C" from "0h = -1 to -255 °C"	58
• Changed to "000h - 0FFh = 0 °C to 255 °C, 100h - 1FFh = -256 °C to -1 °C" from "0h = -1 to -255 °C"	58
• Changed to "000h - 0FFh = 0 °C to 255 °C, 100h - 1FFh = -256 °C to -1 °C" from "0h = -1 to -255 °C"	58
• Changed to "1C0h" from "0h"	63
• Changed to "1C0h" from "0h"	63
• Changed to "1C0h" from "0h"	63
• Changed to "1C0h" from "0h"	64
• Changed to "2882h" from "882h"	64
• Changed to "2h" from "0h"	64
• Changed to "2h" from "0h"	64
• Changed Description of FSM_LIVE_STATUS	65
• Changed to " $V_{IN(min)} \times D / (f_{sw} \times L)$ " from " $V_{IN(min)} \times D / f_{sw} \times L$ "	70
• Changed to "Current set resistor for 100 mA maximum" from "Maximum current set resistor"	73
• Changed to "43.5 V" from "40 V"	75
• Changed to "10-µF" from "10-µH"	78
• Changed to "Current set resistor for 150 mA max" from "Maximum current set resistor"	80

The datasheet number will be changing.

Device Family	Change From:	Change To:
LP8863-Q1	SNVSAB6A	SNVSAB6B

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/LP8863-Q1>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

LP8863ADCPRQ1			
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
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