



## PRODUCT BULLETIN

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**ISSUE DATE:** 01-Jul-2016  
**NOTIFICATION:** 17219  
**TITLE:** MPC5746C Errata Update to Revision 4  
**EFFECTIVE DATE:** 02-Jul-2016

### DEVICE(S)

MPN
SPC5745BK1MMH6
SPC5745BK1MMH6R
SPC5746BK1MMJ6
SPC5746BK1MMJ6R
SPC5746BSK1MKU2
SPC5746BSK1MKU2R
SPC5746BSK1VKU2
SPC5746BSK1VKU2R
SPC5746CK1MMJ6
SPC5746CK1MMJ6R
SPC5746CSK1MKU6
SPC5746CSK1MMH6
SPC5746CSK1MMH6R
SPC5746CSK1MMJ6
SPC5746CSK1MMJ6R
SPC5746CSK1MMN6

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### AFFECTED CHANGE CATEGORIES

- ERRATA

### DESCRIPTION OF CHANGE

The MPC5746C errata documentation has been updated to Revision 4.

The wording of the following errata have been added:

Errata number	Errata Title
e10327	ADC: Incorrect channel under measure is indicated after sampling phase of conversion until conversion completes
E10323	MC_ME: The transition from DRUN/RUN mode to STANDBY will not complete if a wake-up is triggered in a 50nS window.
e10447	MC_ME & LPU: The transition between DRUN/RUN mode to STANDBY or DRUN/RUN mode to LPU_RUN may not complete if a reset is asserted.
e10340	NEXUS: Trace messages following reset are corrupted when the system is reset during Nexus trace operation
e10362	MC_ME & LPU: The transition between DRUN/RUN mode to STANDBY or DRUN/RUN mode to LPU_RUN may not complete if any LVD is asserted or PORST goes low
e10361	MC_ME & LPU: The transition between DRUN/RUN mode to STANDBY or DRUN/RUN mode to LPU_RUN may not complete if EXR is asserted.
e10368	FlexCAN: Transition of the CAN FD operation enable bit may lead FlexCAN logic to an inconsistent state.

The following erratum has been revised:

Errata number	Errata Title
e10132	LPU: Mode transition to LPU_STOP or LPU_STANDBY may not complete

The following erratum has been removed:

Errata number	Errata Title
E9994	SSCM: sscm asserts destructive reset if ECC error is detected during HSM scanning

The updated errata documentation is attached to this notice and can be found at: [http://www.nxp.com/products/microcontrollers-and-processors/power-architecture-processors/mpc5xxx-5xxx-32-bit-mcus/mpc57xx-mcus/ultra-reliable-mcus-for-automotive-industrial-control-and-gateway.MPC574xB-C-D-G?fsp=1&tab=Documentation\\_Tab](http://www.nxp.com/products/microcontrollers-and-processors/power-architecture-processors/mpc5xxx-5xxx-32-bit-mcus/mpc57xx-mcus/ultra-reliable-mcus-for-automotive-industrial-control-and-gateway.MPC574xB-C-D-G?fsp=1&tab=Documentation_Tab)

### REASON FOR CHANGE

The errata documentation for the MPC5746C has been updated.

**ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)**

No changes have been made to the current production device. The errata describe existing conditions identified on current production device. There are potential hardware and/or software implications to customers.

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**NOTE:**

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Support Case](#). Be aware that after you select this link to enter your request, you must choose the topic "Product Change Notification" once on the Salesforce page.

For sample inquiries - please go to [www.nxp.com](http://www.nxp.com)

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**QUALIFICATION STATUS:** N/A

**QUALIFICATION PLAN:**

N/A

**RELIABILITY DATA SUMMARY:**

N/A

**ELECTRICAL CHARACTERISTIC SUMMARY:**

N/A

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**CHANGED PART IDENTIFICATION:**

N/A

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**ATTACHMENT(S):**

External attachment(s) FOR this notification can be viewed AT:

[17219\\_ZVEI\\_DeQuMa - MPC5746C \(Calypso 3M\) - Errata\\_Update\\_GM\\_PB\\_17219 - June2016.pdf](#)  
[17219\\_MPC5746C\\_1N06M\\_Errata\\_July\\_2016\\_Rev\\_4.pdf](#)

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