

P-Channel 12-V (D-S) MOSFET

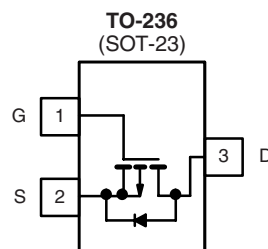
PRODUCT SUMMARY		
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
- 12	0.051 at $V_{GS} = - 4.5$ V	- 4.0
	0.070 at $V_{GS} = - 2.5$ V	- 3.5
	0.106 at $V_{GS} = - 1.8$ V	- 3.0

FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFETs: 1.8 V Rated



RoHS
COMPLIANT
HALOGEN
FREE
Available



Top View
Si2335DS (E5)*
*Marking Code

Ordering Information: Si2335DS-T1-E3 (Lead (Pb)-free)
Si2335DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted				
Parameter	Symbol	5 s	Steady State	Unit
Drain-Source Voltage	V_{DS}	- 12		V
Gate-Source Voltage	V_{GS}	± 8		
Continuous Drain Current ($T_J = 150$ °C) ^{a, b}	$T_A = 25$ °C	- 4.0	- 3.2	A
	$T_A = 70$ °C	- 3.3	- 2.6	
Pulsed Drain Current	I_{DM}	- 15		
Continuous Source Current (Diode Conduction) ^{a, b}	I_S	- 1.6		
Maximum Power Dissipation ^{a, b}	$T_A = 25$ °C	1.25	0.75	W
	$T_A = 70$ °C	0.8	0.48	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 5$ s	R_{thJA}	75	100	°C/W
	Steady State		120	166	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	40	50	

Notes:
a. Surface mounted on 1" x 1" FR4 board.
b. Pulse width limited by maximum junction temperature.

SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -10\text{ }\mu\text{A}$	-12			V
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.45			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -9.6\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -9.6\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq -5\text{ V}, V_{GS} = -4.5\text{ V}$	-15			A
		$V_{DS} \leq -5\text{ V}, V_{GS} = -2.5\text{ V}$	-6			
Drain-Source On-Resistance ^a	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -4.0\text{ A}$		0.042	0.051	Ω
		$V_{GS} = -2.5\text{ V}, I_D = -3.5\text{ A}$		0.058	0.070	
		$V_{GS} = -1.8\text{ V}, I_D = -2.0\text{ A}$		0.082	0.106	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -5\text{ V}, I_D = -4.0\text{ A}$		7		S
Diode Forward Voltage	V_{SD}	$I_S = -1.6\text{ A}, V_{GS} = 0\text{ V}$			-1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -6\text{ V}, V_{GS} = -4.5\text{ V}, I_D \cong -4.0\text{ A}$		9	15	nC
Gate-Source Charge	Q_{gs}			1.9		
Gate-Drain Charge	Q_{gd}			1.5		
Input Capacitance	C_{iss}	$V_{DS} = -6\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		1225		pF
Output Capacitance	C_{oss}			260		
Reverse Transfer Capacitance	C_{rss}			130		
Switching^c						
Turn-On Time	$t_{d(on)}$	$V_{DD} = -6\text{ V}, R_L = 6\text{ }\Omega$ $I_D \cong -1.0\text{ A}, V_{GEN} = -4.5\text{ V}, R_G = 6\text{ }\Omega$		13.0	20	ns
	t_r			15	25	
Turn-Off Time	$t_{d(off)}$			50	70	
	t_f			19	35	

Notes:

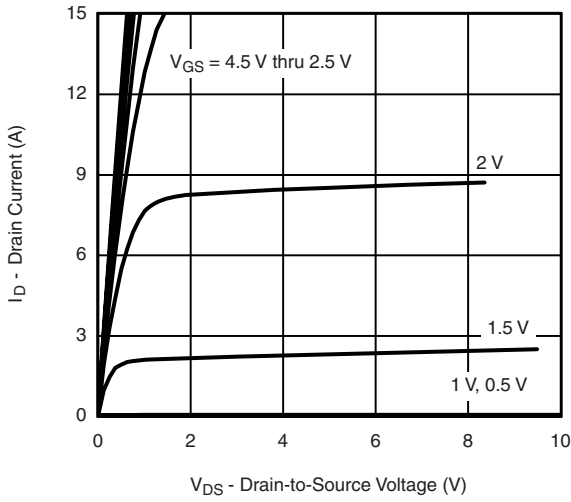
a. Pulse test: $PW \leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. For design aid only, not subject to production testing.

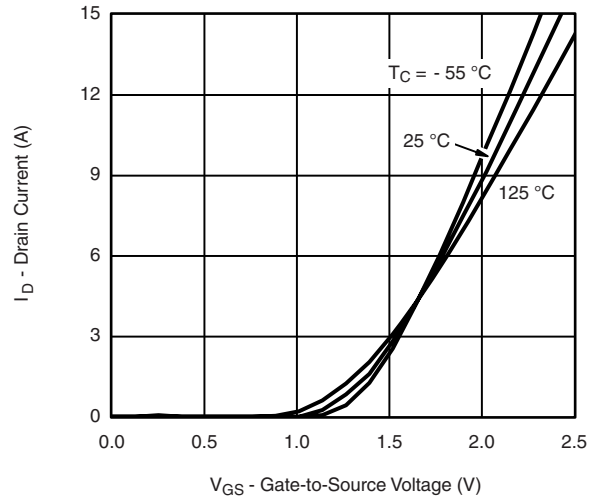
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

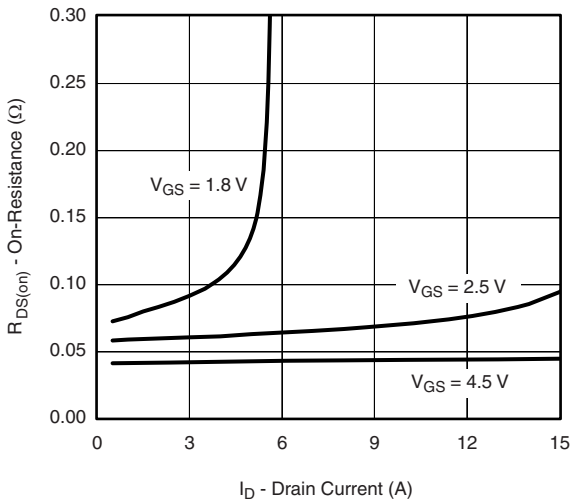
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



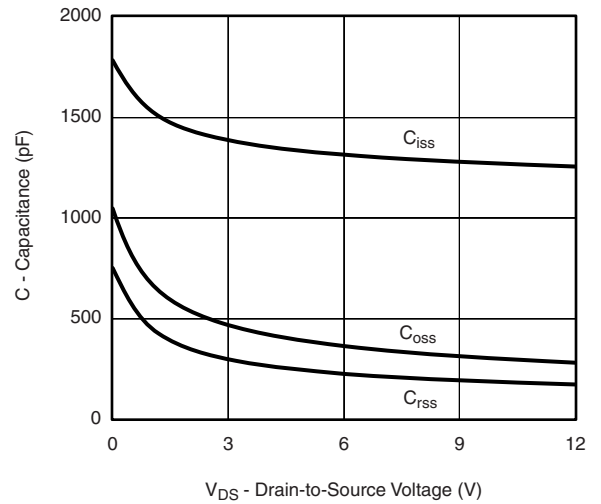
Output Characteristics



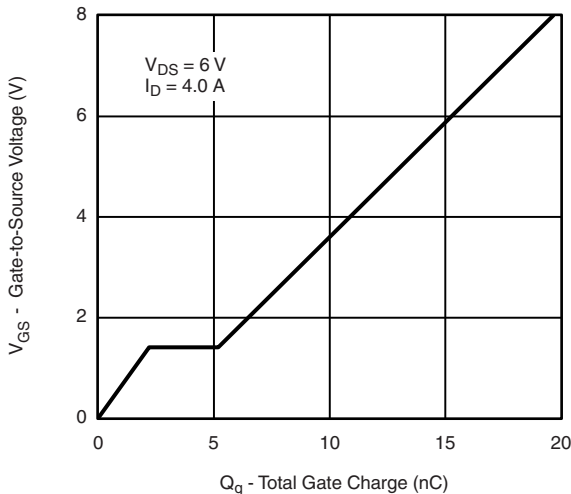
Transfer Characteristics



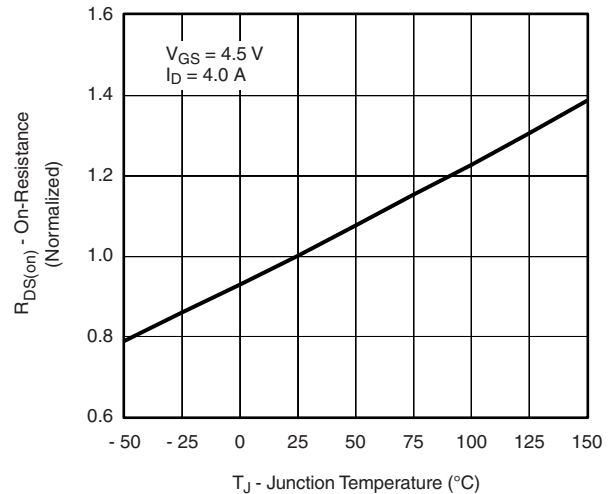
On-Resistance vs. Drain Current



Capacitance

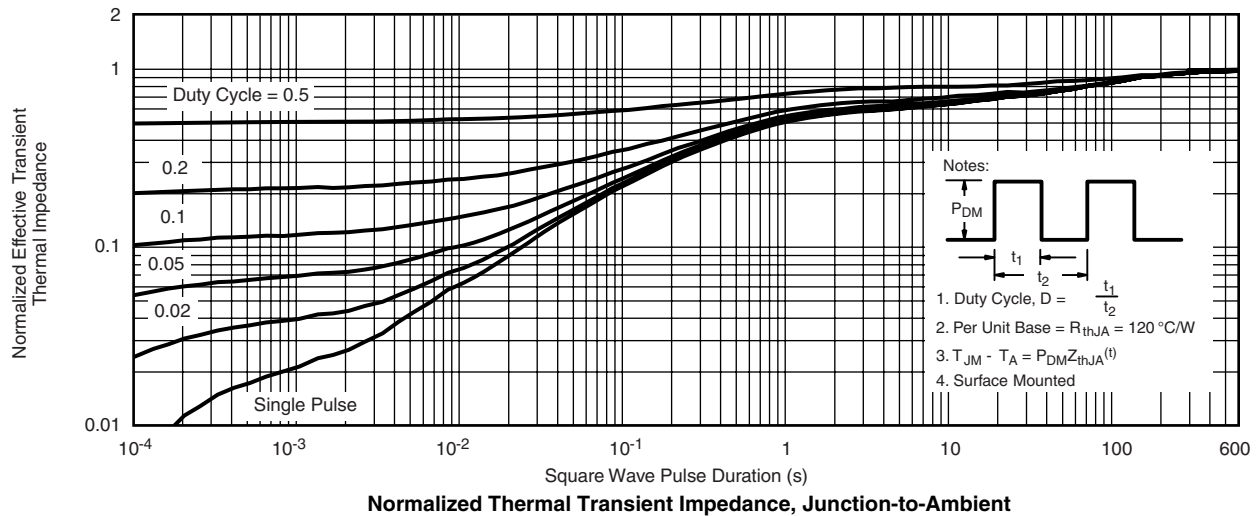
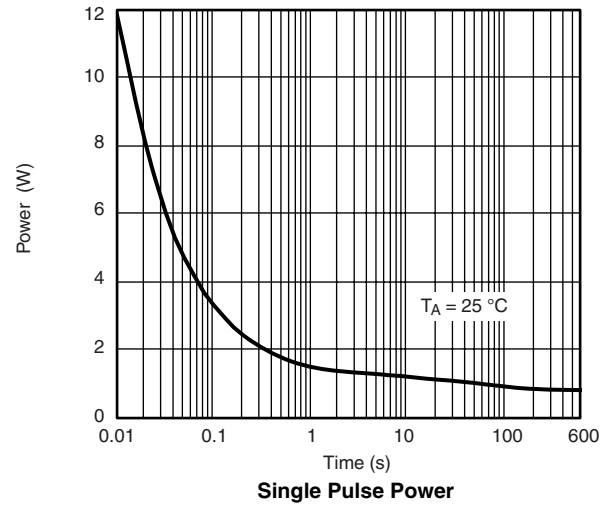
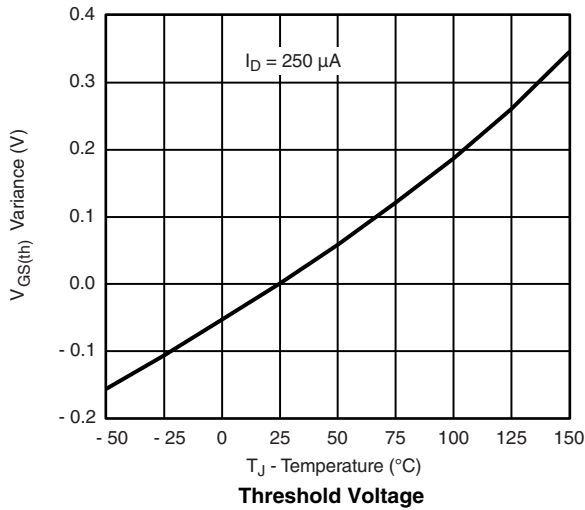
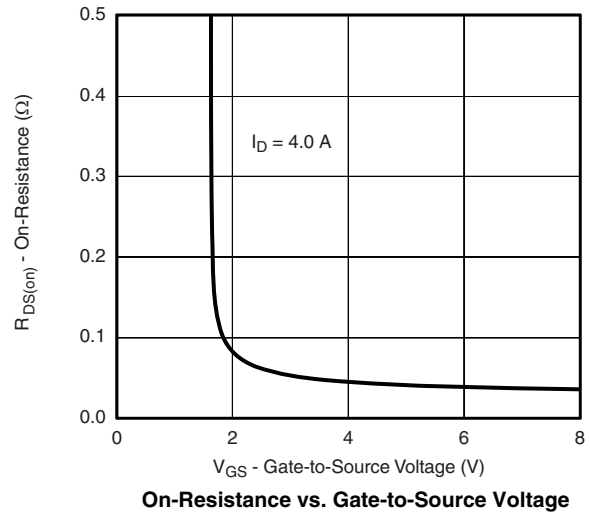
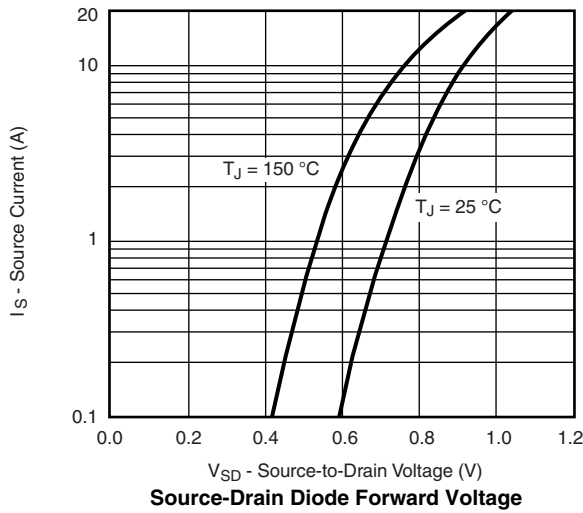


Gate Charge



On-Resistance vs. Junction Temperature

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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